

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/725,318

Filed: 12/2/2003

Inventor(s):  
Katsuki Hazama

Title: SEMICONDUCTOR DEVICE  
INCLUDING TRANSISTOR  
WITH COMPOSITE GATE  
STRUCTURE AND  
TRANSISTOR WITH  
SINGLE GATE STRUCTURE,  
AND METHOD FOR  
MANUFACTURING THE  
SAME

Examiner: Booth, Richard A.  
Group/Art Unit: 2812  
Atty. Dkt. No: 5957-98500

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Dean M. Munyon

Printed Name

  
Signature

February 21, 2006  
Date

**FEE AUTHORIZATION**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

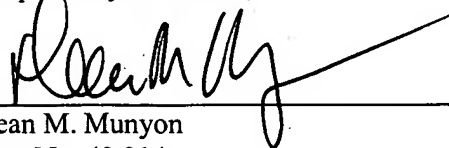
The Commissioner is hereby authorized to charge the following fee to Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C. Deposit Account Number 501505/5957-98500

Petition Under 37 C.F.R. §1.136 for Extension of Time for 2 months	\$450.00
Total:	\$450.00

Attorney Docket No.: 5957-98500

The Commissioner is also authorized to charge any extension fee or other fees that may be necessary to the same account number.

Respectfully submitted,



Dean M. Munyon

Reg. No. 42,914

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Date: February 21, 2006